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Docket No.: M-12562 US

February 14, 2002

Box Patent Application Commissioner For Patents Washington, D. C. 20231

Enclosed herewith for filing is a patent application, as follows:

Inventor(s):

Richard J. Nathan, Dale E. Means Jr.

Title:

Integrated Circuit Package and Method For Fabrication

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This Transmittal Letter (in duplicate) page(s) Specification (not including claims)

page(s) Claims

page Abstract

Sheet(s) of Drawings consisting of Figs. 1a, 1b, 2a, 2b, 3a, 3b, 4a, 4b, 5a, and 5b page(s) Declaration for Patent Application and Power of Attorney (signed-in-counterpart)

Applicant(s) assert(s) entitlement to small entity status for the attached patent application

CLAIMS AS FILED (fees computed under 37 CFR §1.9(f))

<u>For</u> Total Claims	Number <u>Filed</u> 29	-20	==	Number <u>Extra</u> 9	x	<u>Rate</u> \$ 9.00	=	\$ \$	Basic Fee <u>370.00</u> 81.00
Independent Claims	3	-3	=	0	х	\$42.00	=	\$	0.00
Fee of for the first filing of one or more multiple dependent claims per application								\$	
Fee for Request for Extension of Time								\$	
Please make the following charges to Deposit Account 19-2386: Total fee for filing the patent application in the amount of The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.							\$ be	451.00	

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

Reg m. 33,928

Alan H. MacPherson Attorney for Applicants

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